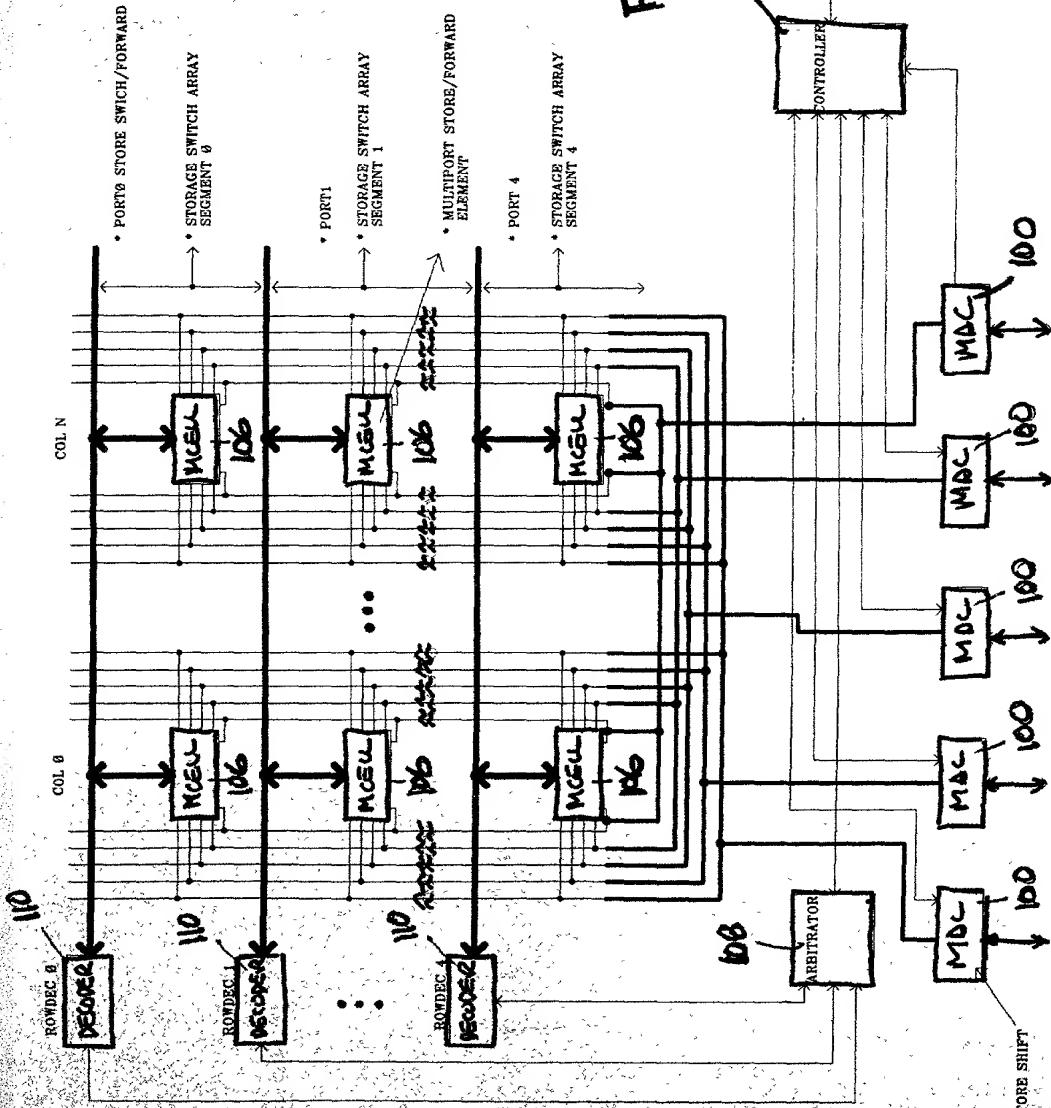
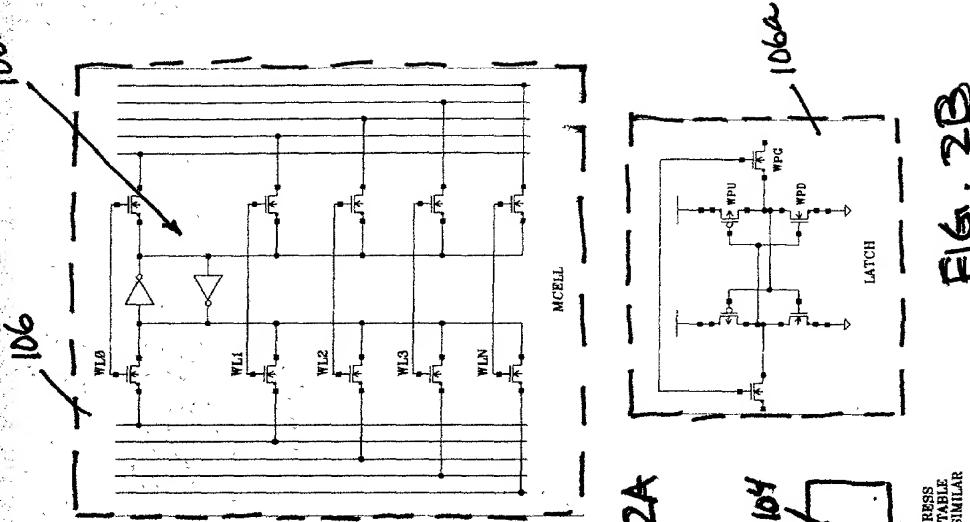


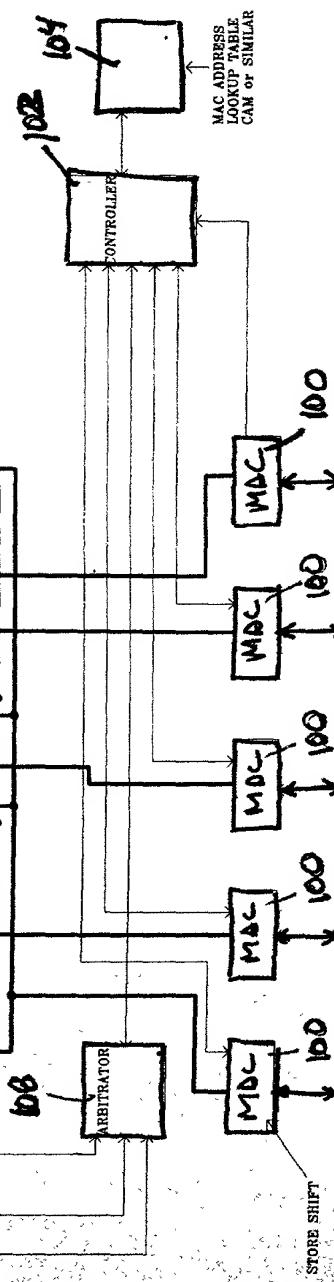
Fig. 1  
(prior ART)



**FIG. 2A**



**FIG. 2B**



**FIG. 2**

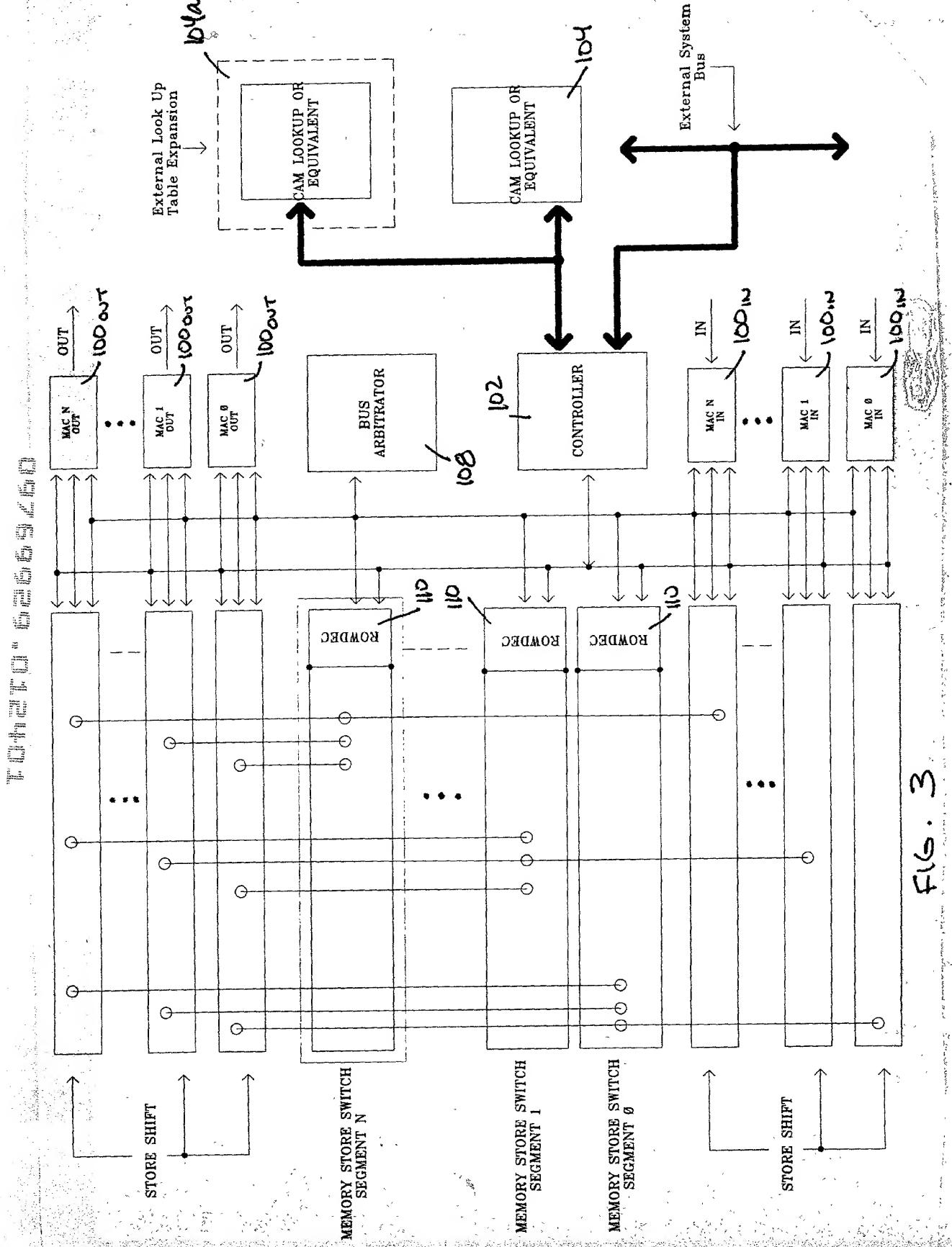


Fig. 3

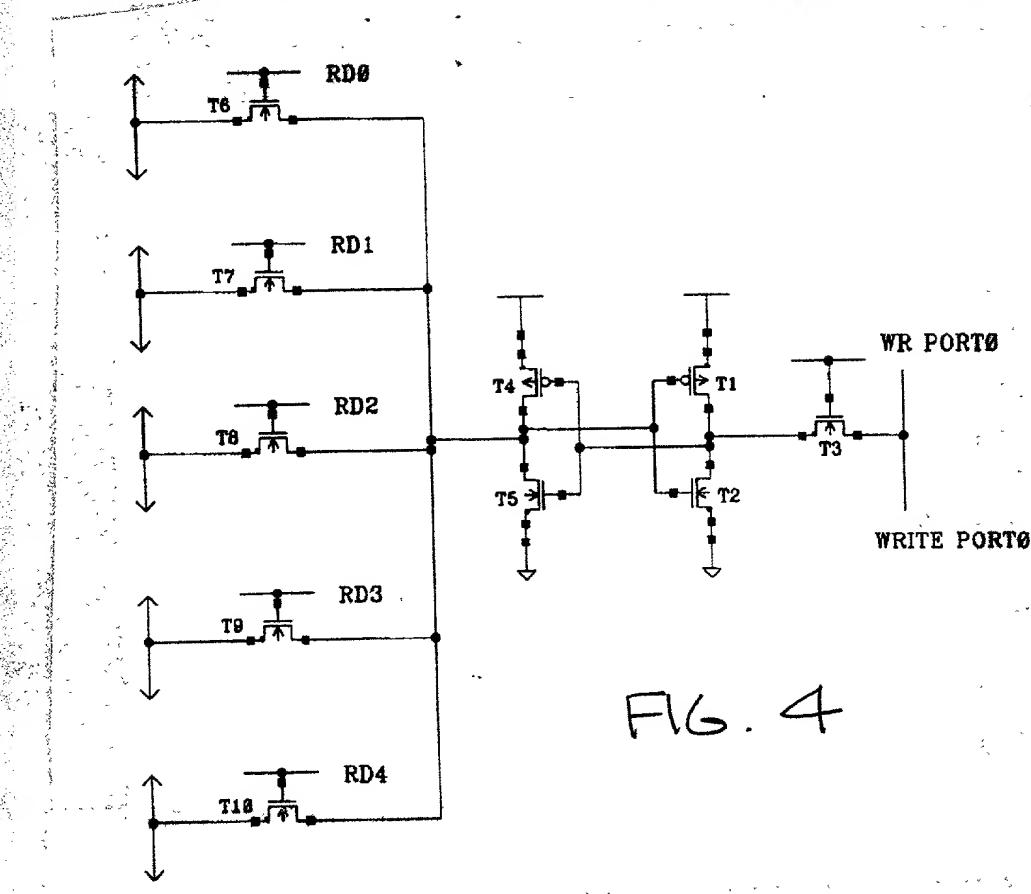


FIG. 4

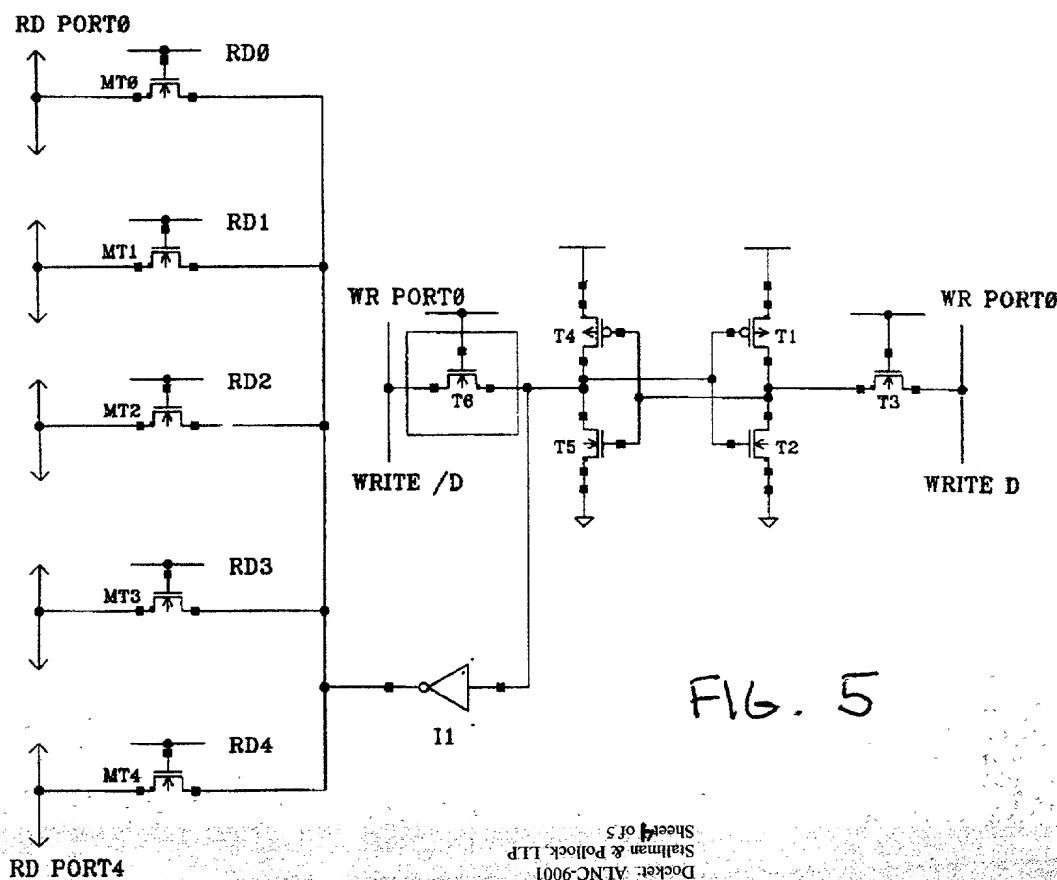


FIG. 5

Invertors: Nandut et al.  
Title: An Integrated High Speed Switch Router Using a  
Multiport Arbiter  
Address: Micrel Inc., P.O. Box 9001  
Express Mail No.: ELL742693US  
Stileman & Pollock, LLP  
Decoder: ALNC-9001  
Sheet 4 of 5

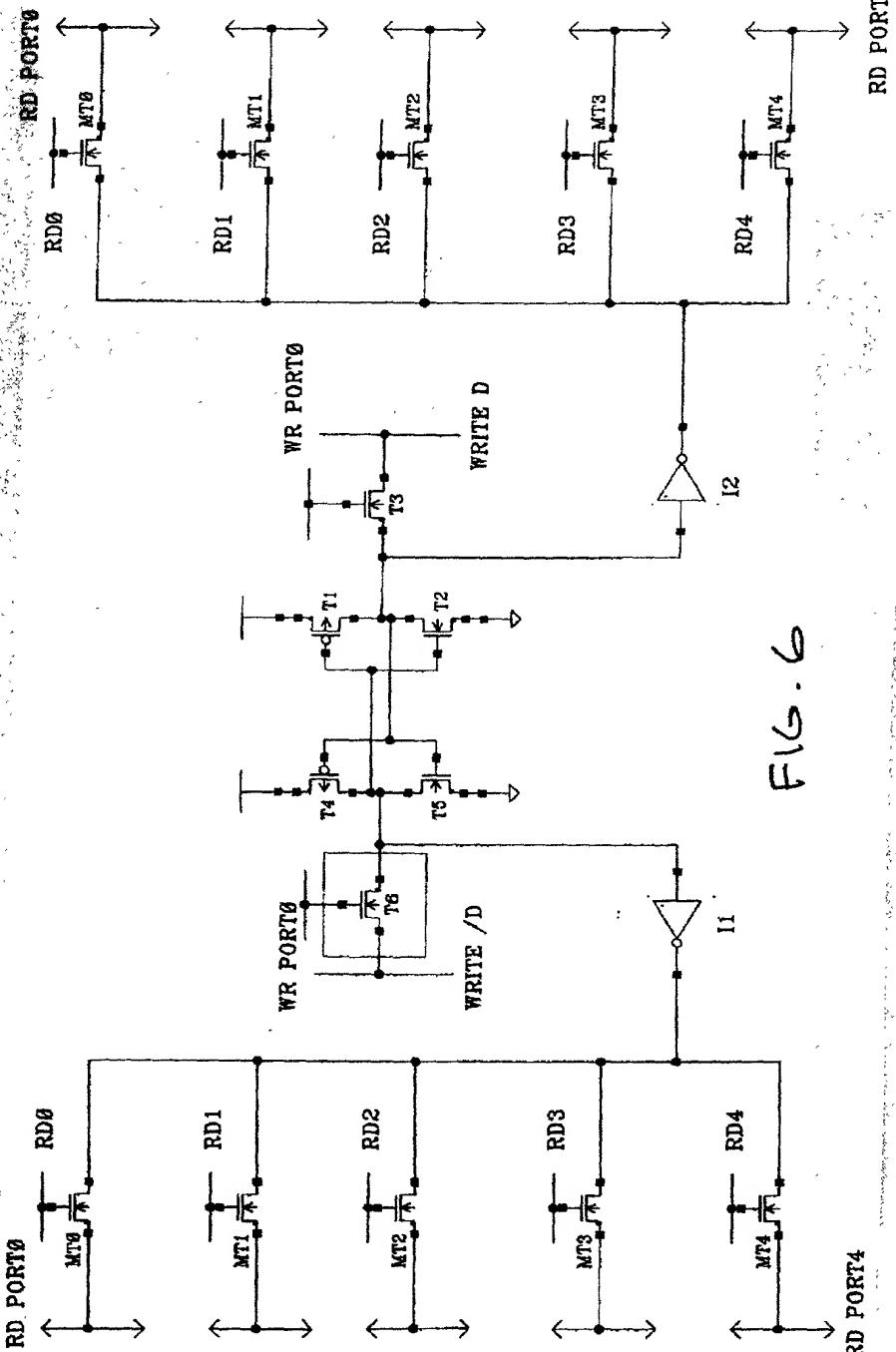


Fig. 6